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U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

TRANSMITTAL LETTER

Docket Number:
2885/21Application Number
09/335,974Filing Date
June 18, 1999

Examiner

Art Unit
2751Invention Title
**I/O AND MEMORY BUS SYSTEM FOR DFPS
AND UNITS WITH TWO- OR MULTI-
DIMENSIONAL PROGRAMMABLE CELL
ARCHITECTURES**Inventor(s)
VORBACH et al.Address to:
Assistant Commissioner for Patents
Box: Missing Parts
Washington D.C. 20231I hereby certify that this correspondence is being deposited with the
United States Postal Service as first class mail in an envelope addressed
to: Assistant Commissioner for Patents, Washington, D.C. 20231 on

Date:

14 Dec 1999

Reg. No. 36,098

Signature:

Michelle M. Carniaux

Please find enclosed the following documents filed in connection with the
above-referenced application:


1. Response to Missing Parts of Application - Filing Date Granted
2. Declaration.
3. Assignment from the Inventors to PACT GmbH, with Recordation Form Cover Sheet.
4. Appointment of Power of Attorney by Assignee of Entire Interest
5. Verified Statement Claiming Small Entity Status
6. The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to the deposit account of **Kenyon & Kenyon**, deposit account number **11-0600**:
 - A. Any additional filing fees required under 37 C.F.R. § 1.16;



- B. Any additional patent application processing fees under 37 C.F.R. § 1.17;
 - C. Any additional document supply fees under 37 C.F.R. § 1.19;
 - D. Any additional post-patent processing fees under 37 C.F.R. § 1.20; or
 - E. Any additional miscellaneous fees under 37 C.F.R. § 1.21.
7. A duplicate copy of this letter is enclosed for charging purposes.

Dated: 14 Dec 1997

By:


Michelle M. Carniaux
(Reg. No. 36,098)

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